

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Not for submission under 37 CFR 1.99)	Application Number	09989765
	Filing Date	2001-11-19
	First Named Inventor	Manfred Bartz
	Art Unit	2176
	Examiner Name	Maikhanh Nguyen
	Attorney Docket Number	CD01194M

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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## **NON-PATENT LITERATURE DOCUMENTS**

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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
/M.N./	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>
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**EXAMINER SIGNATURE**

Examiner Signature	/Maikhanh Nguyen/	Date Considered	12/17/2009
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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## **U.S.PATENT APPLICATION PUBLICATIONS**

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/M.N./	1	20020156998	A1	2002-10-24	Casselmann, Steven	Entire Document
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/M.N./	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
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/M.N./	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
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Application Number	09989765
Filing Date	2001-11-19
First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD01194M

/M.N./	26	USPTO Non-Final Rejection for 10/008,096 (CD01212M) dated 06/24/05; 15 pages	<input type="checkbox"/>
	27	USPTO Non-Final Rejection for 10/008,096 (CD01212M) dated 12/12/07; 14 pages	<input type="checkbox"/>
	28	USPTO Notice of Allowance for 10/008,096 (CD01212M) dated 12/22/08; 24 pages	<input type="checkbox"/>
	29	USPTO Notice of Allowance for 09/975,105 (CD00184) dated 12/04/06; 4 pages	<input type="checkbox"/>
	30	USPTO Final Rejection for 09/975,105 (CD00184) dated 07/13/06; 7 pages	<input type="checkbox"/>
	31	USPTO Non-Final Rejection for 09/975,105 (CD00184) dated 01/19/06; 5 pages	<input type="checkbox"/>
	32	USPTO Non-Final Rejection for 09/975,105 (CD00184) dated 04/19/05; 9 pages	<input type="checkbox"/>
	33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Monte Mar, filed on 08/29/2001; 46 pages	<input type="checkbox"/>
	34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Perrin et al., filed on 09/09/2002; 41 pages	<input type="checkbox"/>
	35	USPTO Notice of Allowance for 10/324,455 (CD98087C2) dated 02/12/2004; 4 pages	<input type="checkbox"/>
/M.N./	36	USPTO Non-Final Rejection for 10/324,455 (CD98087C2) dated 11/06/2003; 4 pages	<input type="checkbox"/>

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/M.N./	37	USPTO Non-Final Rejection for 10/324,455 (CD98087C2) dated 08/21/2003; 4 pages	<input type="checkbox"/>
	38	USPTO Notice of Allowance for 09/998,859 (CD01209M) dated 03/14/2005; 6 pages	<input type="checkbox"/>
	39	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 11/04/2004; 6 pages	<input type="checkbox"/>
	40	USPTO Final Rejection for 09/998,859 (CD01209M) dated 11/19/2003; 5 pages	<input type="checkbox"/>
	41	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 05/28/2003; 6 pages	<input type="checkbox"/>
	42	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 05/15/2003; 6 pages	<input type="checkbox"/>
	43	USPTO Notice of Allowance for 09/998,834 (CD01221M) dated 05/19/2005; 7 pages	<input type="checkbox"/>
	44	USPTO Non-Final Rejection for 09/998,834 (CD01221M) dated 09/20/2004; 11 pages	<input type="checkbox"/>
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	46	USPTO Final Rejection for 10/113,065 (CD01215M) dated 10/26/2005; 17 pages	<input type="checkbox"/>
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/M.N./	48	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Monte Mar, filed on 12/09/1998; 23 pages	<input type="checkbox"/>
/M.N./	49	USPTO Notice of Allowance for Application Number 10/001,477 (CD1203M) dated 05/08/2009; 6 pages	<input type="checkbox"/>

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## U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/M.N./	1	6553057	B1	2003-04-22	Sha	Entire Document
	2	5399922	B1	1995-03-21	Kiani et al.	Entire Document
	3	5680070	B1	1997-10-21	Anderson et al.	Entire Document
	4	6445211	B1	2002-09-03	Saripella	Entire Document
	5	5202687	B1	1993-04-13	Distinti	Entire Document
	6	6166367	B1	2000-12-26	Cho	Entire Document
	7	5600262	B1	1997-02-04	Kolze	Entire Document
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/M.N./	9	5258760	B1	1993-11-02	Moody et al.	Entire Document
	10	5563526	B1	1996-10-08	Hastings et al.	Entire Document
	11	6225866	B1	2001-05-01	Kubota et al.	Entire Document
	12	5880598	B1	1999-03-09	Duong	Entire Document
	13	6304101	B1	2001-10-16	Nishihara	Entire Document
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/M.N./	20	6157270	B1	2000-12-05	Tso	Entire Document
	21	4138671	B1	1997-02-06	Comer et al.	Entire Document
	22	5633766	B1	1997-05-27	Hase et al.	Entire Document
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	25	5903718	B1	1999-05-11	Marik	Entire Document
	26	6188975	B1	2001-02-13	Gay	Entire Document
	27	6263302	B1	2001-07-17	Hellestrand, et al.	Entire Document
	28	6332201	B1	2001-12-18	Chin, et al.	Entire Document
	29	6598178	B1	2003-07-22	Yee, et al.	Entire Document
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/M.N./	31	5438672	B1	1995-08-01	Dey	Entire Document
	32	6314530	B1	2001-11-06	Mann	Entire Document
	33	6681280	B1	2004-01-20	Miyake, et al.	Entire Document
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	36	6611276	B1	2003-08-26	Muratori et al.	Entire Document
	37	6823497	B1	2004-11-23	Schubert et al.	Entire Document
	38	6865429	B1	2005-03-08	Schneider et al.	Entire Document
	39	6664978	B1	2003-12-16	Kekic et al.	Entire Document
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	43	6211741	B1	2001-04-03	Dalmia	Entire Document
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**U.S. PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/M.N./	1	20040054821	A1	2004-03-18	Warren et al.	Entire Document

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Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
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/M.N./	1	06021732	JP	A1	1994-01-28	Kadowaki, Tadao	Entire Document	<input type="checkbox"/>
	2	0450863A2	EP	A1	1991-10-09	Austin	Entire Document	<input type="checkbox"/>
	3	0499383A2	EP	A1	1992-08-19	El Ayat; Khaled, A.	Entire Document	<input type="checkbox"/>
	4	0308583A2	EP	A1	1989-03-29	Bartlett	Entire Document	<input type="checkbox"/>
	5	0639816A2	EP	A1	1995-02-22	McCollum	Entire Document	<input type="checkbox"/>
	6	1170671A1	EP	A1	2002-01-08	Arena et al.	Entire Document	<input type="checkbox"/>
	7	1205848	EP	A1	2002-05-15	Ericsson (Plesner)	Entire Document	<input type="checkbox"/>
	8	1191423A2	EP	A1	2003-02-01	Garnell I Andreu	Entire Document	<input type="checkbox"/>
	9	404083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>
	10	404095408	JP	A1	2002-03-27	Nishimaki	Entire Document	<input type="checkbox"/>
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## NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
/M.N./	1	Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/1994; 10 pages	<input type="checkbox"/>
	2	Harbaum, T. et al. "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999; 10 pages	<input type="checkbox"/>
	3	USPTO Application Number 09/924,734 (CD00169): "Programmable Microcontroller (PSoC) Architecture (Mixed Analog/Digital)"; Snyder et al., filed on 08/07/01; 28 pages	<input type="checkbox"/>
	4	USPTO Application Number 09/909,045 (CD00170): "Digital Configurable Macro Architecture," Warren Snyder, filed on 07/18/2001; 37 pages	<input type="checkbox"/>
	5	USPTO Application Number 09/909,109 (CD00172): "Configuring Digital Functions in a Digital Configurable Macro Architecture," Warren Snyder, filed on 07/18/2001; 38 pages	<input type="checkbox"/>
	6	USPTO Application Number 09/909,047 (CD00173): "A Programmable Analog System Architecture," Monte Mar, filed on 07/18/01; 60 pages	<input type="checkbox"/>
	7	USPTO Application Number 09/930,021 (CD00174): "Programmable Methodology and Architecture for a Programmable Analog System"; Mar et al., filed on 08/14/2001; 87 pages	<input type="checkbox"/>
/M.N./	8	USPTO Application Number 09/969,311 (CD00175): "Method for Synchronizing and Resetting Clock Signals Supplied to Multiple Programmable Analog Blocks," Bert Sullam, filed on 10/01/01; 57 pages	<input type="checkbox"/>

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/M.N./	9	USPTO Application Number 09/875,599 (CD00180): "Method and Apparatus for Programming a Flash Memory," Warren Snyder, filed on 06/05/2001; 23 pages	<input type="checkbox"/>
	10	USPTO Application Number 09/975,115 (CD00182): "In-System Chip Emulator Architecture," Snyder et al., filed on 10/10/2001; 38 pages	<input type="checkbox"/>
	11	USPTO Application Number 09/953,423 (CD00187): "A Configurable Input/Output interface for a Microcontroller," Warren Snyder, filed on 09/14/2001; 28 pages	<input type="checkbox"/>
	12	USPTO Application Number 09/893,050 (CD00199): "Multiple Use of Microcontroller Pad," Kutz et al., filed on 06/26/2001; 21 pages	<input type="checkbox"/>
	13	USPTO Application Number 09/929,891 (CD00226): "Programming Architecture for a Programmable Analog System," Mar et al., filed on 08/14/2001; 82 pages	<input type="checkbox"/>
	14	USPTO Application Number 09/969,313 (CD00227): "Architecture for Synchronizing and Resetting Clock Signals Supplied to Multiple Analog Programmable Analog Blocks," Bert Sullam, filed on 10/01/2001; 50 pages	<input type="checkbox"/>
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	17	USPTO Non-Final Rejection for 09/994,601 (CD01170M) dated 09/21/2005; 12 pages	<input type="checkbox"/>
	18	USPTO Final Rejection for 09/994,601 (CD01170M) dated 03/24/2005; 10 pages	<input type="checkbox"/>
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/M.N./	20	GANAPATHY ET AL., "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996; 4 pages	<input type="checkbox"/>
	21	The Provisional Application 60/243,708 (CD00167P) "Advanced Programmable Microcontroller Device"; Snyder et al., filed on 10/26/2000; 277 pages	<input type="checkbox"/>
	22	"Webster's Third New International Dictionary", 1996, G. & C. Merriam Company; 3 pages (including pages 1328-1329)	<input type="checkbox"/>
	23	USPTO Advisory Action for 09/998,848 (CD01177) dated 09/07/05; 3 pages	<input type="checkbox"/>
	24	USPTO Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability," Mar et al., filed on 09/19/2001; 28 pages	<input type="checkbox"/>
	25	USPTO Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies," Sullam et al., filed on 10/25/2001; 49 pages	<input type="checkbox"/>
	26	USPTO Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode," Warren Snyder, filed on 10/05/2001; 31 pages	<input type="checkbox"/>
	27	USPTO Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface," Warren Snyder, filed on 10/05/2001; 32 pages	<input type="checkbox"/>
	28	USPTO Application Number 09/972,133 (CD00178): "Method for Entering Circuit Test Mode," Warren Snyder, filed on 10/05/2001; 30 pages	<input type="checkbox"/>
	29	USPTO Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm," Warren Snyder, filed on 10/09/2001; 26 pages	<input type="checkbox"/>
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/M.N./	31	USPTO Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture," Warren Snyder, filed on 10/15/2002; 36 pages	<input type="checkbox"/>
	32	USPTO Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 05/09/2005; 1 page	<input type="checkbox"/>
	33	USPTO Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks," Warren Snyder, filed on 05/14/2001; 28 pages	<input type="checkbox"/>
	34	USPTO Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller," Kutz et al., filed on 06/22/2001; 44 pages	<input type="checkbox"/>
	35	USPTO Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller," Sullam et al., filed on 10/24/2001; 34 pages	<input type="checkbox"/>
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	39	USPTO Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System," Craig Nemecek, filed on 11/01/2001; 43 pages	<input type="checkbox"/>
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/M.N./	42	USPTO Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller," Kutz et al., filed on 06/22/2001; 42 pages	<input type="checkbox"/>
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/M.N./	48	USPTO Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit," Warren Snyder, filed on 08/06/2001; 25 pages	<input type="checkbox"/>

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	4	6161199	B1	2000-12-12	Szelo et al.	Entire Document
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First Named Inventor	Manfred Bartz	
Art Unit	2176	
Examiner Name	Malkhanh Nguyen	
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	17	Bauer et al.; "A Reconfigurable Logic Machine for Fast Event-Driven Simulation"; June 1998; Design Automation Conference Proceedings; 8 pages	<input type="checkbox"/>
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/M.N./	19	USPTO Advisory Action for 09/994,601 dated 5/23/06 (CD01170M); 3 pages	<input type="checkbox"/>

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Application Number		09989765
Filing Date		2001-11-19
First Named Inventor	Manfred Bartz	
Art Unit	2176	
Examiner Name	Maikhanh Nguyen	
Attorney Docket Number	CD01194M	

/M.N./	20	USPTO Non-Final Rejection for 09/994,601 dated 7/29/04 (CD01170M); 10 pages	<input type="checkbox"/>
	21	USPTO Ex Parte Quayle Action for 09/992,076 dated 06/18/07 (CD01210M); 6 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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/M.N./	1	20050240917	A1	2005-10-27	Wu, Yuh-Cherng	Entire Document

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	28	ITO, SERGIO AKIRA AND CARRO, LUIGI; "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000; 6 pages	<input type="checkbox"/>
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/M.N./	30	Monte Mar, Bert Sullam, Eric Blom; "An architecture for a configurable Mixed-signal device", 2003, IEEE Journal of Solid-State Circuits, Volume 3; 4 pages	<input type="checkbox"/>
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	33	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	<input type="checkbox"/>
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number		09989765
Filing Date		2001-11-19
First Named Inventor	Manfred Bartz	
Art Unit	2176	
Examiner Name	Maikhanh Nguyen	
Attorney Docket Number	CD01194M	

/M.N./	1	20040205695	A1	2004-10-14	Fletcher, David	Entire Document
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## FOREIGN PATENT DOCUMENTS

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## NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
/M.N./	1	USPTO Notice of Allowance for Application Number 09/930,021 (CD00174) dated 11/26/2004; 4 pages	<input type="checkbox"/>
	2	USPTO Final Rejection for Application Number 09/930,021 (CD00174) dated 08/31/2004; 8 pages	<input type="checkbox"/>
	3	USPTO Non-Final Rejection for Application Number 09/930,021 (CD00174) dated 04/26/2004; 6 pages	<input type="checkbox"/>
	4	USPTO Miscellaneous Action with SSP for Application Number 09/930,021 (CD00174) dated 10/01/2001; 1 page	<input type="checkbox"/>
/M.N./	5	USPTO Notice of Allowance for Application Number 09/953,423 (CD00187) dated 07/12/2004; 6 pages	<input type="checkbox"/>

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Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD01194M

/M.N./	6	USPTO Non-Final Rejection for Application Number 09/953,423 (CD00187) dated 02/06/2004; 5 pages	<input type="checkbox"/>
	7	USPTO Notice of Allowance for Application Number 09/957,084 (CD00201) dated 05/18/2004; 5 pages	<input type="checkbox"/>
	8	USPTO Final Rejection for Application Number 09/957,084 (CD00201) dated 01/29/2004; 8 pages	<input type="checkbox"/>
	9	USPTO Non-Final Rejection for Application Number 09/957,084 (CD00201) dated 08/27/2003; 8 pages	<input type="checkbox"/>
	10	USPTO Final Rejection for Application Number 09/957,084 (CD00201) dated 04/23/2003; 8 pages	<input type="checkbox"/>
	11	USPTO Non-Final Rejection for Application Number 09/957,084 (CD00201) dated 08/23/2002; 6 pages	<input type="checkbox"/>
	12	USPTO Notice of Allowance for Application Number 09/969,313 (CD00227) dated 10/04/2005; 6 pages	<input type="checkbox"/>
	13	USPTO Non-Final Rejection for Application Number 09/969,313 (CD00227) dated 05/06/2005; 9 pages	<input type="checkbox"/>
	14	USPTO Requirement for Restriction/Election for Application Number 09/969,313 (CD00227) dated 03/18/2005; 6 pages	<input type="checkbox"/>
	15	USPTO Notice of Allowance for Application Number 09/969,311 (CD00175) dated 03/01/2005; 4 pages	<input type="checkbox"/>
/M.N./	16	USPTO Non-Final Rejection for Application Number 09/969,311 (CD00175) dated 09/21/2004; 4 pages	<input type="checkbox"/>

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	18	USPTO Final Rejection for Application Number 09/969,311 (CD00175) dated 04/07/2003; 7 pages	<input type="checkbox"/>
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	21	USPTO Non-Final Rejection for Application Number 09/972,319 (CD00179) dated 09/16/2004; 9 pages	<input type="checkbox"/>
	22	USPTO Notice of Allowance for Application Number 09/972,003 (CD00176) dated 07/14/2004; 4 pages	<input type="checkbox"/>
	23	USPTO Requirement for Restriction/Election for Application Number 09/972,003 (CD00176) dated 05/06/2004; 4 pages	<input type="checkbox"/>
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	25	USPTO Non-Final Rejection for Application Number 09/972,003 (CD00176) dated 08/19/2003; 11 pages	<input type="checkbox"/>
	26	USPTO Notice of Allowance for Application Number 09/972,133 (CD00178) dated 06/09/2006; 6 pages	<input type="checkbox"/>
M.N./	27	USPTO Final Rejection for Application Number 09/972,133 (CD00178) dated 03/30/2006; 8 pages	<input type="checkbox"/>

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Attorney Docket Number	CD01194M

/M.N./	28	USPTO Non-Final Rejection for Application Number 09/972,133 (CD00178) dated 11/25/2005; 9 pages	<input type="checkbox"/>
	29	USPTO Advisory Action for Application Number 09/972,133 (CD00178) dated 08/31/2005; 3 pages	<input type="checkbox"/>
	30	USPTO Final Rejection for Application Number 09/972,133 (CD00178) dated 06/29/2005; 10 pages	<input type="checkbox"/>
	31	USPTO Non-Final Rejection for Application Number 09/972,133 (CD00178) dated 03/08/2005; 9 pages	<input type="checkbox"/>
	32	USPTO Notice of Allowance for Application Number 09/975,104 (CD00183) dated 10/19/2006; 6 pages	<input type="checkbox"/>
	33	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 06/16/2006; 8 pages	<input type="checkbox"/>
	34	USPTO Final Rejection for Application Number 09/975,104 (CD00183) dated 02/15/2006; 7 pages	<input type="checkbox"/>
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	36	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 03/21/2005; 7 pages	<input type="checkbox"/>
	37	USPTO Notice of Allowance for Application Number 09/975,030 (CD00185) dated 02/06/2006; 7 pages	<input type="checkbox"/>
/M.N./	38	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 10/20/2005; 7 pages	<input type="checkbox"/>



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Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD01194M

/M.N./	39	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 03/29/2005; 13 pages	<input type="checkbox"/>
	40	USPTO Notice of Allowance for Application Number 09/977,111 (CD00202) dated 09/28/2006; 6 pages	<input type="checkbox"/>
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	42	USPTO Final Rejection for Application Number 10/272,231 (CD00170C) dated 11/05/2003; 5 pages	<input type="checkbox"/>
	43	USPTO Non-Final Rejection for Application Number 10/272,231 (CD00170C) dated 07/14/2003; 6 pages	<input type="checkbox"/>
	44	USPTO Notice of Allowance for Application Number 11/125,554 (CD00230C) dated 02/07/2008; 4 pages	<input type="checkbox"/>
	45	USPTO Notice of Allowance for Application Number 11/125,554 (CD00230C) dated 04/24/2007; 7 pages	<input type="checkbox"/>
	46	USPTO Non-Final Rejection for Application Number 11/125,554 (CD00230C) dated 12/11/2006; 9 pages	<input type="checkbox"/>
	47	USPTO Notice of Allowance for Application Number 09/855,868 (CD00181) dated 04/25/2005; 6 pages	<input type="checkbox"/>
	48	USPTO Non-Final Rejection for Application Number 09/855,868 (CD00181) dated 08/26/2004; 7 pages	<input type="checkbox"/>
/M.N./	49	USPTO Notice of Allowance for Application Number 09/887,923 (CD00229) dated 09/27/2004; 5 pages	<input type="checkbox"/>

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Art Unit	2176
Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD01194M

/M.N./	50	USPTO Non-Final Rejection for Application Number 09/887,923 (CD00229) dated 05/25/2004; 7 pages	<input type="checkbox"/>
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**EXAMINER SIGNATURE**

Examiner Signature	/Maikhanh Nguyen/	Date Considered	12/17/2009
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number	09989765
	Filing Date	2001-11-19
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	Art Unit	2176
	Examiner Name	Maikhanh Nguyen
	Attorney Docket Number	CD01194M

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Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/M.N./ ↓	1	6934674	B1	2005-08-23	Douezzy et al.	Entire Document
	2	6051772	B1	2000-04-18	Cameron et al.	Entire Document
	3	6016554	B1	2000-01-18	Skrovan et al.	Entire Document
	4	6704889	B1	2004-03-09	Veenstra et al.	Entire Document
	5	5978937	B1	1999-11-02	Miyamori et al.	Entire Document
	6	5317202	B1	1994-05-31	Waizman	Entire Document
	7	6121805	B1	2000-09-19	Thamsirianunt et al.	Entire Document
/M.N./	8	6211708	B1	2001-04-03	Klemmer	Entire Document

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STATEMENT BY APPLICANT**  
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Application Number	09989765
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First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD011194M

/M.N./	9	6396302	B1	2002-05-28	New et al.	Entire Document
	10	6204687	B1	2001-03-20	Schutiz et al.	Entire Document
	11	5996032	B1	1999-11-30	Baker	Entire Document
	12	5608892	B1	1997-03-04	Wakerly	Entire Document
	13	5808883	B1	1998-09-15	Hawkes	Entire Document
	14	5949632	B1	1999-09-07	Barreras Sr. et al.	Entire Document
	15	6542025	B1	2003-04-01	Kutz et al.	Entire Document
	16	5554951	B1	1996-09-10	Gough	Entire Document
	17	5894243	B1	1999-04-13	Hwang	Entire Document
	18	6081140	B1	2000-06-27	King	Entire Document
/M.N./	19	6104217	B1	2000-08-15	Magana	Entire Document

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Application Number	09989765
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Art Unit	2176
Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD01194M

/M.N./	20	6286127	B1	2001-09-04	King et al.	Entire Document
	21	6166960	B1	2000-12-26	Marneweck et al.	Entire Document
	22	5982105	B1	1999-11-09	Masters	Entire Document
	23	4740966	B1	1988-04-26	Goad	Entire Document
	24	6643151	B1	2003-11-04	Nebregic et al.	Entire Document
	25	6003133	B1	1999-12-14	Moughanni et al.	Entire Document
	26	6339815	B1	2002-01-15	Feng et al.	Entire Document
	27	6574739	B1	2003-06-03	Kung et al.	Entire Document
	28	5737760	B1	1998-04-07	Grimmer et al.	Entire Document
	29	5969632	B1	1999-10-19	Diamant et al.	Entire Document
/M.N./	30	6009496	B1	1999-12-28	Tsai	Entire Document

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Examiner Name	Maikhanh Nguyen
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/M.N./	31	6839774	B1	2005-01-04	Ahn et al.	Entire Document
	32	6754765	B1	2004-06-22	Chang et al.	Entire Document
	33	5630057	B1	1997-05-13	Hait	Entire Document
	34	6324628	B1	2001-11-27	Chan	Entire Document
	35	6236593	B1	2001-05-22	Hong et al.	Entire Document
	36	6219729	B1	2001-04-17	Keats et al.	Entire Document
	37	5319771	B1	1994-06-07	Takeda	Entire Document
	38	5378935	B1	1995-01-03	Korhonen et al.	Entire Document
	39	5572719	B1	1996-11-05	Biestlerfeldt	Entire Document
	40	5579353	B1	1996-11-26	Parmenter et al.	Entire Document
/M.N./	41	5696952	B1	1997-12-09	Pontarelli	Entire Document

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Examiner Name	Maikhanh Nguyen
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/M.N./	42	5774704	B1	1998-06-30	Williams	Entire Document
/M.N./	43	5986479	B1	1999-11-16	Mohan, Jitendra	Entire Document
/M.N./	44	6603348	B1	2003-08-05	Preuss et al.	Entire Document

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**U.S.PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code*	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/M.N./	1	20040153802	A1	2004-08-05	Kudo et al.	Entire Document
	2	20020059543	A1	2002-05-16	Cheng et al.	Entire Document
	3	20010010083	A1	2001-07-26	Satoh	Entire Document
	4	20020133794	A1	2002-09-19	Kanapathipillai et al.	Entire Document
	5	20020065646	A1	2002-05-30	Waldie et al.	Entire Document
/M.N./	6	20020133771	A1	2002-09-19	Barnett	Entire Document

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/M.N./	1	USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2006; 4 pages	<input type="checkbox"/>
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	17	USPTO Non-Final Rejection for Application Number 10/004,039 (CD01222M) dated 04/11/2006; 14 pages	<input type="checkbox"/>
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	42	USPTO Notice of Allowance for Application Number 09/922,579 (CD00230) dated 12/28/2004; 6 pages	<input type="checkbox"/>
	43	USPTO Non-Final Rejection for Application Number 09/922,579 (CD00230) dated 08/18/2004; 6 pages	<input type="checkbox"/>
	44	USPTO Notice of Allowance for Application Number 09/923,461 (CD00192) dated 05/12/2005; 6 pages	<input type="checkbox"/>
	45	USPTO Non-Final Rejection for Application Number 09/923,461 (CD00192) dated 07/16/2004; 6 pages	<input type="checkbox"/>
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number	09989765
	Filing Date	2001-11-19
	First Named Inventor	Manfred Bartz
	Art Unit	2176
	Examiner Name	Maikhanh Nguyen
	Attorney Docket Number	CD01194M

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Application Number	09989765
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First Named Inventor	Manfred Bartz
Art Unit	2176
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Attorney Docket Number	CD01194M

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Application Number	09989765
Filing Date	2001-11-19
First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD01194M

/M.N./	42	5952888	B1	1999-09-14	Scott	Entire Document
↓	43	6026134	B1	2000-02-15	Duffy et al.	Entire Document
	44	6172571	B1	2001-01-09	Moyal et al.	Entire Document
	45	6211741	B1	2001-04-03	Dalmia	Entire Document
	46	6275117	B1	2001-08-14	Abugharbieh et al.	Entire Document
	47	6307413	B1	2001-10-23	Dalmia et al.	Entire Document
	48	6310521	B1	2001-10-30	Dalmia	Entire Document
↓	49	6377646	B1	2002-04-23	Sha	Entire Document
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**FOREIGN PATENT DOCUMENTS**

Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
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/M.N./	1	USPTO Application Number 11/850,260 (CD2007006): "Circuit and Method for Improving the Accuracy of a Crystal-less Oscillator Having Dual-Frequency Modes," Wright et al., filed on 09/05/2007; 33 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 11/850,260 (CD2007006) dated 03/06/2009; 7 pages	<input type="checkbox"/>
	3	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 03/09/2009; 11 pages	<input type="checkbox"/>
	4	USPTO Advisory Action for Application Number 11/644,100 (CD05204) dated 02/09/2009; 3 pages	<input type="checkbox"/>
	5	USPTO Final Rejection for Application Number 11/644,100 (CD05204) dated 11/18/2008; 12 pages	<input type="checkbox"/>
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M.N./	8	USPTO Advisory Action for Application Number 11/415,588 (CD05028) dated 01/14/2008; 3 pages	<input type="checkbox"/>
	9	USPTO Final Rejection for Application Number 11/415,588 (CD05028) dated 10/19/2007; 8 pages	<input type="checkbox"/>
	10	USPTO Non-Final Rejection for Application Number 11/415,588 (CD05028) dated 06/13/2007; 8 pages	<input type="checkbox"/>
	11	USPTO Notice of Allowance for Application Number 12/218,404 (CD05028C1) dated 03/19/2009; 7 pages	<input type="checkbox"/>
	12	USPTO Non-Final Rejection for Application Number 12/218,404 (CD05028C1) dated 09/30/2008; 8 pages	<input type="checkbox"/>
	13	USPTO Application Number 11/644,100 (CD05204): "Differential-to-single ended signal converter circuit and method," Jonathon Stiff, filed on 12/21/2006; 33 pages	<input type="checkbox"/>
	14	USPTO Application Number 11/415,588 (CD05028): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan et al., filed on 05/01/2006; 24 pages	<input type="checkbox"/>
	15	USPTO Application Number 12/218,404 (CD05028C1): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan et al., filed on 07/14/2008; 23 pages	<input type="checkbox"/>
	16	USPTO Notice of Allowance for Application Number 11/132,894 (CD04064) dated 04/26/2007; 4 pages	<input type="checkbox"/>
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/M.N./	19	USPTO Non-Final Rejection for Application Number 11/322,044 (CD05198) dated 05/04/2009; 18 pages	<input type="checkbox"/>
	20	USPTO Non-Final Rejection for Application Number 11/322,044 (CD05198) dated 11/25/2008; 15 pages	<input type="checkbox"/>
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	22	USPTO Advisory Action for Application Number 11/322,044 (CD05198) dated 11/30/2007; 2 pages	<input type="checkbox"/>
	23	USPTO Final Rejection for Application Number 11/322,044 (CD05198) dated 09/21/2007; 14 pages	<input type="checkbox"/>
	24	USPTO Non-Final Rejection for Application Number 11/322,044 (CD05198) dated 04/24/2007; 13 pages	<input type="checkbox"/>
	25	USPTO Application Number 11/322,044 (CD05198): "Split charge pump PLL architecture," Jonathon Stiff, filed on 12/28/2005; 19 pages	<input type="checkbox"/>
	26	USPTO Notice of Allowance for Application Number 10/305,589 (CD02181) dated 02/04/2005; 5 pages	<input type="checkbox"/>
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/M.N./	30	USPTO Application Number 09/849,164 (CD01035): "Reduced Static Phase Error CMOS PLL Charge Pump," Jonathon Stiff, filed on 05/04/2001; 30 pages	<input type="checkbox"/>
	31	Maneatis, "Low-Jitter Process-Independent DLL and PLL Based on Self-Biased Techniques," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996; 10 pages	<input type="checkbox"/>
	32	Larsson, "A 2-1600-MHz CMOS Clock Recovery PLL with Low-V <sub>dd</sub> Capability," IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999; 10 pages	<input type="checkbox"/>
	33	USPTO Notice of Allowance for Application Number 10/327,217 (CD02174) dated 08/12/2004; 6 pages	<input type="checkbox"/>
	34	USPTO Non-Final Rejection for Application Number 10/327,217 (CD02174) dated 04/30/2004; 5 pages	<input type="checkbox"/>
	35	USPTO Miscellaneous Action for Application Number 10/327,217 (CD02174) dated 02/10/2004; 1 page	<input type="checkbox"/>
	36	USPTO Application Number 10/327,217 (CD02174): "Single Ended Clock Signal Generator Having a Differential Output," Richmond et al., filed on 12/20/2002; 27 pages	<input type="checkbox"/>
	37	USPTO Notice of Allowance for Application Number 10/871,582 (CD03036) dated 03/30/2006; 6 pages	<input type="checkbox"/>
	38	USPTO Final Rejection for Application Number 10/871,582 (CD03036) dated 02/01/2006; 5 pages	<input type="checkbox"/>
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/M.N./	41	USPTO Notice of Allowance for Application Number 09/404,891 (CD99054) dated 03/04/2005; 6 pages	<input type="checkbox"/>
	42	USPTO Final Rejection for Application Number 09/404,891 (CD99054) dated 12/08/2004; 7 pages	<input type="checkbox"/>
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U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/M.N./ ↓	1	6636096	B1	2003-10-21	Schaffer et al.	Entire Document
	2	6560306	B1	2003-05-06	Duffy et al.	Entire Document
	3	6680632	B1	2004-01-20	Meyers et al.	Entire Document
	4	6683930	B1	2004-01-27	Dalmia	Entire Document
	5	6690224	B1	2004-02-10	Moore	Entire Document
	6	6782068	B1	2004-08-24	Wilson et al.	Entire Document
	7	7138868	B1	2006-11-21	Sanchez et al.	Entire Document
/M.N./ ↓	8	6686787	B1	2004-02-03	Ling, Kuok	Entire Document

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/M.N./	9	6617888	B1	2003-09-09	Volk, Andrew M.	Entire Document
	10	6597212	B1	2003-07-22	Wang et al.	Entire Document
	11	5530444	B1	1996-06-25	Tice et al.	Entire Document
	12	7400183	B1	2008-07-15	Sivadasan et al.	Entire Document
	13	7030688	B1	2006-04-18	Dosho et al.	Entire Document
	14	6949984	B1	2005-09-27	Siniscalchi	Entire Document
	15	6917661	B1	2005-07-12	Scott et al.	Entire Document
	16	5355097	B1	1994-10-11	Scott et al.	Entire Document
	17	5745011	B1	1998-04-28	Scott	Entire Document
	18	6903613	B1	2005-06-07	Mitchell et al.	Entire Document
/M.N./	19	6842710	B1	2005-01-11	Gehring et al.	Entire Document

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/M.N./	20	6744323	B1	2004-06-01	Moyal et al.	Entire Document
	21	6704381	B1	2004-03-09	Moyal et al.	Entire Document
	22	6711226	B1	2004-03-23	Williams et al.	Entire Document
	23	6614374	B1	2003-09-02	Gustavsson et al.	Entire Document
	24	6670852	B1	2003-12-30	Hauck	Entire Document
	25	6625765	B1	2003-09-23	Krishnan	Entire Document
	26	6433645	B1	2002-08-13	Mann et al.	Entire Document
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↓ /M.N./	30	6580329	B1	2003-06-17	Sander, Wendell	Entire Document

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/M.N./	31	6806782	B1	2004-10-19	Motoyoshi et al.	Entire Document
	32	3845328	B1	1974-10-01	Hollingsworth	Entire Document
	33	6980060	B1	2005-12-27	Boerstler et al.	Entire Document
	34	6380811	B1	2002-04-30	Zarubinsky et al.	Entire Document
	35	6949984	B1	2005-09-27	Siniscalchi	Entire Document
	36	4980652	B1	1990-12-25	Tarusawa et al.	Entire Document
	37	5206582	B1	1993-04-27	Ekstedt et al.	Entire Document
	38	7132835	B1	2006-11-07	Arcus	Entire Document
	39	3810036	B1	1974-05-01	Bloedorn	Entire Document
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/M.N./	42	6963233	B1	2005-11-08	Puccio et al.	Entire Document
	43	6043695	B1	2000-03-28	O'Sullivan, Eugene	Entire Document
	44	5424689	B1	1995-06-13	Gillig et al.	Entire Document
	45	7015735	B1	2006-03-21	Kimura et al.	Entire Document
	46	6600351	B1	2003-07-29	Bisanti et al.	Entire Document
	47	6940356	B1	2005-09-06	McDonald et al.	Entire Document
	48	6937075	B1	2005-08-30	Lim et al.	Entire Document
	49	6043719	B1	2000-03-28	Lin et al.	Entire Document
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/M.N./	1	USPTO Application Number 10/226,911 (CD02112): "Calibration of Integrated Circuit Time Constants," Gehring et al.; filed on 08/22/2002; 32 pages	<input type="checkbox"/>
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	3	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 08/28/2003; 9 pages	<input type="checkbox"/>
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/M.N./	6	USPTO Application Number 09/943,149 (CD01085): "Method for Phase Locking in a Phase Lock Loop," Moyal et al., filed on 08/30/2001; 21 pages	<input type="checkbox"/>
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	8	Durham et al., "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," IEEE, 1992; 7 pages	<input type="checkbox"/>
	9	Durham et al., "High-Linearity Continuous-Time Filter in 5-V VLSI CMOS," IEEE, 1992; 8 pages	<input type="checkbox"/>
	10	USPTO Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Paul H. Scott, filed on 03/29/1998; 35 pages	<input type="checkbox"/>
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	14	USPTO Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al., filed on 02/26/2002; 28 pages	<input type="checkbox"/>
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/M.N./	17	USPTO Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al., filed on 08/24/2004; 24 pages	<input type="checkbox"/>
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	Examiner Name	Maikhanh Nguyen
	Attorney Docket Number	CD01194M

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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First Named Inventor	Manfred Bartz	
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M.N./	1	USPTO Advisory Action for Application Number 11/200,619 (CD04112) dated 05/11/2009; 3 pages	<input type="checkbox"/>
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/M.N./	8	Written Opinion of the International Search Authority for International Application Number PCT/US08/60680 (CD2007130PCT) dated 08/15/2008; 4 pages	<input type="checkbox"/>
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	10	USPTO Advisory Action for Application Number 09/475,879 (CD99047) dated 03/04/2002; 3 pages	<input type="checkbox"/>
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	12	USPTO Final Rejection for Application Number 09/475,879 (CD99047) dated 10/11/2001; 10 pages	<input type="checkbox"/>
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	14	USPTO Application Number 09/475,879 (CD99047): "Programmable Logic Device," Lacey et al.; filed on 12/30/1999; 50 pages	<input type="checkbox"/>
	15	USPTO Application Number 09/475,808 (CD99069): "Configurable Memory for Programmable Logic Circuits," Lacey et al., filed on 12/30/1999; 24 pages	<input type="checkbox"/>
	16	USPTO Non-Final Rejection for Application Number 09/475,808 (CD99069) dated 06/06/2001; 8 pages	<input type="checkbox"/>
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/M.N./	18	USPTO Application Number 10/137,497 (CD01197M): "Reconfigurable Testing System and Method," Pleis et al.; filed on 05/01/2002; 40 pages	<input type="checkbox"/>

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
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Application Number	09989765
Filing Date	2001-11-19
First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
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/M.N./	19	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 11/05/2004; 17 pages	<input type="checkbox"/>
	20	USPTO Final Rejection for Application Number 10/137,497 (CD01197M) dated 05/05/2005; 13 pages	<input type="checkbox"/>
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	23	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 08/02/2006; 21 pages	<input type="checkbox"/>
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	29	USPTO Application Number 10/172,670 (CD01242M): "Method and System for Programming a Memory Device," Snyder et al.; filed on 06/13/2002; 66 pages	<input type="checkbox"/>

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/M,N/	30	USPTO Application Number 11/986,338 (CD1197C): Reconfigurable Testing System and Method," Pleis et al., filed on 11/20/2007; 41 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	32	6332137	B1	2001-12-18	Hori et al.	Entire Document
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	46	5886582	B1	1999-03-23	Stansell	Entire Document
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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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/M.N./	2	20030097640	A1	2003-05-22	Abrams et al.	Entire Document
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M.N./	1	USPTO Application Number 11/273,708 (CD05141): "Capacitance Sensor Using Relaxation Oscillators," Snyder et al., filed on 11/14/2005; 33 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 11/273,708 (CD05141) dated 03/19/2007; 16 pages	<input type="checkbox"/>
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	5	USPTO Application Number 11/337,272 (CD05191): "Successive Approximate Capacitance Measurement Circuit," Warren Snyder, filed on 01/20/2006 29 pages	<input type="checkbox"/>
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/M.N./	8	USPTO Final Rejection for Application Number 11/337,272 (CD05191) dated 02/02/2007; 11 pages	<input type="checkbox"/>
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	11	USPTO Notice of Allowance for Application Number 11/337,272 (CD05191) dated 08/15/2007; 9 pages	<input type="checkbox"/>
	12	USPTO Application Number 11/983,291 (CD05191D): "Successive Approximate Capacitance Measurement Circuit," Warren Snyder, filed on 11/07/2007; 26 pages	<input type="checkbox"/>
	13	USPTO Non-Final Rejection for Application Number 11/983,291 (CD05191D) dated 03/09/2009; 9 pages	<input type="checkbox"/>
	14	USPTO Application Number 11/698,660 (CD05176): "Configurable Bus," Kutz et al., filed on 01/25/2007; 35 pages	<input type="checkbox"/>
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	20	Sedra et al., "Microelectronic Circuits," 3rd Edition, 1991, Oxford University Press, 02/05/2007; 20 pages	<input type="checkbox"/>
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/M.N./	30	Cypress Semiconductor Corporation, "PSoC CY8C20x34 Technical Reference Manual (TRM)," PSoC CY8C20x34 TRM, Version 1.0, 2006; 220 pages	<input type="checkbox"/>
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First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD01194M

/M.N./	31	5530673	B1	1996-06-25	Tobita et al.	Entire Document
	32	7358714	B1	2008-04-15	Watanabe et al.	Entire Document
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**U.S. PATENT APPLICATION PUBLICATIONS**

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/M.N./	1	20080095213	A1	2008-04-24	Lin et al.	Entire Document

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/M.N./	2	20080259998	A1	2008-10-23	Venkataraman et al.	Entire Document
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/M.N./	1	DE 19710829	DE	A1	1998-09-24	Goloub, Boris	Entire Document	<input type="checkbox"/>

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	9	Written Opinion of the International Searching Authority for International Application Number PCT/US08/60695 (CD2007132) dated 07/22/2009; 6 pages	<input type="checkbox"/>
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